IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Bonkee KIM et al.

Serial No.: New Div. of 10/051,290

Filed: Herewith

For: SINGLE-ENDED DIFFERENTAL

CIRCUIT USING COMPLENTARY

DEVICES

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents Washington, DC 20231

Sir:

In accordance with MPEP 609 and 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants bring the attached references to the Examiner's attention and request that they be considered and made of record in this application. Also attached is a completed PTO-1449 form. This Information Disclosure Statement is being filed within three (3) months of the filing date of this application. Thus, *no fee is* believed to be due for this Information Disclosure Statement. However, the Commissioner is authorized to charge any fees associated with this communication or credit any overpayment to Deposit Account No. 50-1836. A duplicate copy of this Statement is enclosed for that purpose.

Prompt examination on the merits is respectfully requested.

Respectfully submitted,

Todd M. Guise Reg. No. 46,748

SHINJYU GLOBAL IP COUNSELORS, LLP 1233 Twentieth Street, NW, Suite 700 Washington, DC 20036 Phone No. (202)-293-0444

Dated: 11/18/03

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Examiner

Approved for use through 10/31/2002. OMB 0551-0031 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	New Div. of 10/051,290	
				Filing Date	Herewith	
				First Named Inventor	Bonkee KIM	
				Group Art Unit		
(use as many sheets as necessary)			necessary)	Examiner Name		
Sheet	1	of	1	Attorney Docket Number	GK-US015199-B	

	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS							
1 using One-third Frequency Local Oscillator, 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp			of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume- issue number(s), publisher, city and/or country where published.	Translation (Yes or No)				
		1	using One-third Frequency Local Oscillator, 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp					

Signature		Considered	
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Date

fered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.